

Patent 7,319,346

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No. 7,319,346

Docket No. 02M99.05CON2

Issue Date: January 15, 2008

Patentee: You-Yuh Shyr et al.

Title: CIRCUIT AND EMTHOD FOR TRIMMING INTEGRATED CIRCUITS

REQUEST FOR CERTIFICATION OF CORRECTION

Attn: Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 2231301450

It is requested that a Certificate of Correction be issued correcting printing errors appearing in the above-identified United State Patent. A copy of the text of the Certificate in the suggested form is enclosed.

Issuance of the Certificate of Correction would neither expand nor contract the scope of the claims as properly allowed, and re-examination is not required.

As the error is that of the Patent Office, it is believed that no fee is due.

Respectfully submitted,

Customer Number 32047

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UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO : 7,319,346

DATED : January 15, 2008

INVENTOR(S) : Shyr et al.

It is certified that error appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 14, line 26, in Claim 14, delete "ladding" and insert -- adding --, therefor.

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